

Abstract of the Disclosure

This invention describes a false sync code protection (FSP) decoding technique by software for removing padding binary numbers from video data signals in electronic devices such as camera-phones. These padding binary numbers (e.g., a
5 byte 10100101_2) are encoded in the video data signal. The invention describes a fast method for carrying out the decoding process using a predetermined criterion without need for separately comparing the received data using bit-by-bit process, and thus significantly reducing the number of processor instructions compared to the conventional bit-by-bit method. For the presented example these instructions are
10 executed for every stand-alone zero byte found within e.g. JPEG data, which leads to considerable savings in execution time, especially if several JPEG images are taken and processed consecutively.